





In re PATENT application of)
Masaaki HIROKI et al.)
Serial No. 07/993,391) Group Art Unit: 2609
Filed: December 18, 1992) Examiner: L. Lao
For: ELECTRO-OPTICAL DEVICE)
cı	ERTIFICATE OF MAILING
United States Postal Servi	this correspondence is being deposited with the with sufficient postage as First Class Maito: Commissioner of Patents and Trademarks April 4, 1994.
Deborah T. Tomme	
Honorable Commissioner of Patents and Washington, D.C. 20231 Sir:	Trademarks
Transmitted herewith is an a application.	mendment in the above-identifie
	application under 37 C.F.R. 1.9 and by a verified statement previously
[] A verified statement to e C.F.R. 1.9 and 1.27 is er	stablish small entity status under 3 nclosed.
[X] No additional fee is require	red.
[] A check in the amount of enclosed.	\$ to cover the filing fee i

- [X] In the event applicant(s) has overlooked the need for any petition and fee for extension of time, and such extension is required, applicant(s) requests that this be considered a petition therefor and that such fee be charged to Deposit Account No. 19-2380 (0756-819).
- [X] The Commissioner is hereby authorized to charge fees under 37 CFR 1.16 and 1.17 (except the Issue Fee) which may be required now or hereafter, or credit any overpayment, to Deposit Account No. 19-2380 (0756-819). A duplicate copy of this sheet is attached.

Respectfully submitted,

Gerald J. Ferguson, Jr

Reg. No. 23,016

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Docket: 0756-819

Ch APR 12 PH 2: 44.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Rose 4-694

Examiner L. Lao
Art Unit No. 260

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Deborah D. Jonne

AMENDMENT

Honorable Commissioner of Patents and Trademarks

ELECTRO-OPTICAL DEVICE

Washington, D.C. 20231

Sir:

In response to the Official Action dated January 4, 1994, please amend the above identified application as follows.

IN THE CLAIMS

Please amend claims 33, 34 and 35 as follows:

33. The device of claim 31 wherein said image data production means includes latch means operably connected with a data line for latching data received from said data line [a latch circuit].

BI